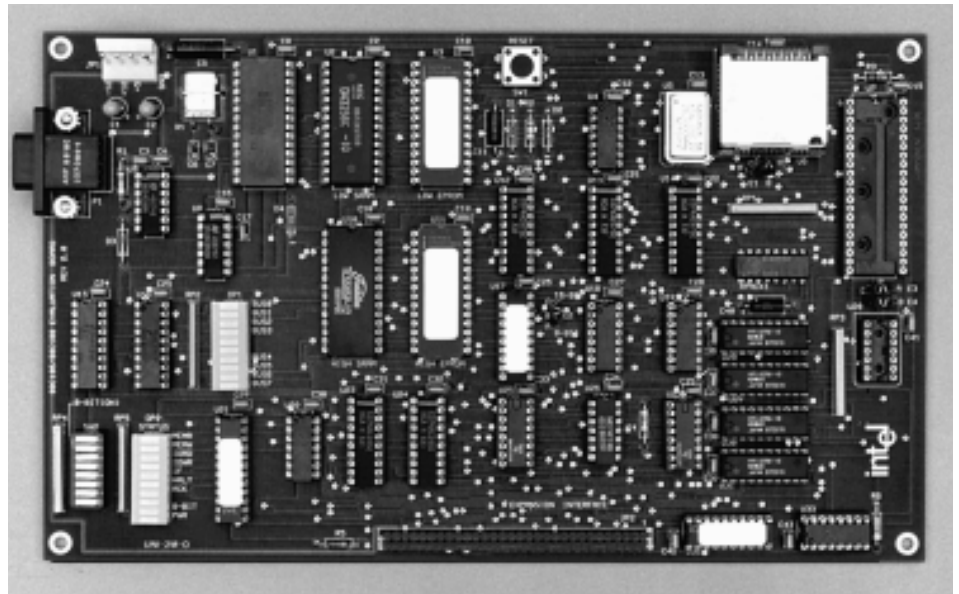


## EV80C186EA/XL, EV80C186EB/EC Evaluation Boards

- Full-Speed Execution
  - 20 MHz, One Wait State (EV80C186EA/XL)
  - 16 MHz, Zero Wait State (EV80C186EB)
  - 16 MHz, Zero Wait State (EV80C186EC)
- 32K Bytes (Expandable) of SRAM/ROMsim
- 512K Bytes of DRAM
- All CMOS Board For Low Power
- Supports Intel Flash Memory
- Concurrent Interrogation of Memory and Registers
- 16 Software Breakpoints
- 2 Single-Step Modes
- High-Level Language Support
- Paradigm or Microsoft Debugger (With Kit Only)
- RS-232C Communication Link
- Easily Reconfigured to Support 8-Bit Processor

### Low-Cost Code-Evaluation Tool

Intel's EV80C186EA/XL, and EV80C186EB and EV80C186EC evaluation boards provide a hardware environment for code execution and software debugging at a relatively low cost. Each board features an advanced CHMOS<sup>®</sup>, 16-bit embedded processor that is a member of the industry standard 80186 family. The boards allow you to take full advantage of the power of the 80C186 architecture. All boards support powerdown and idle-mode operation for development of power-sensitive applications. The EV80C186EB features two independent serial channels providing a serial link for easy interprocessor communications, diagnostic and modem interfacing for today's "mobile office." Their memory (ROMsim) can be reconfigured to match your planned memory system, allowing for exact analysis of code execution speeds in a particular application. Popular features such as a symbolic single-line assembler/disassembler, single-step program execution, and 16 software breakpoints are standard.



Each evaluation board is hosted on an IBM PC or BIOS-compatible clone, already a standard development solution in most of today's engineering environments. The source code for the on-board monitor (written in ASM-86) is public domain. The program is about 2K bytes in length and can be easily modified for inclusion in your target hardware. In addition, third-party vendors can provide retargetable debuggers to further enhance your debug and development process.

### Personal Computer Requirements

The EV80C186EA/XL, EV80C186EB, and EV80C186EC evaluation boards are hosted on an IBM PC, XT, AT, or BIOS-compatible clone. The PC must meet the following minimum requirements:

- 512K Bytes of memory
- One 360K byte floppy-disk drive
- PC DOS v3.1 or later
- A serial port (COM 1 or COM2) at 9600 baud
- Language software

### CONTACT:

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